L Number	Hits	Search Text	DB	Time stamp
1	600	(computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727	USPAT	2003/07/01 12:27
2	118	((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and ((source adj address) and (destination adj address))	USPAT	2003/07/01 12:46
3	4	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and ((source adj address) and (destination adj address))) and ((bit-wise near address) or (bit adj increment) or ((bit adj "1") near address) or ((increase near bit) near address))	USPAT	2003/07/01 12:45
4	0	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and ((source adj address) and (destination adj address))) and (bit-wise near address)	USPAT	2003/07/01 12:44
5	4	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and ((source adj address) and (destination adj address))) and (bit adj increment)	USPAT	2003/07/01 12:44
6	0	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and ((source adj address) and (destination adj address))) and ((increase near bit) near address)	USPAT	2003/07/01 12:45
7	69	((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode"	USPAT	2003/07/01 13:54
10	0	((((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode") and ((source adj address) and (destination adj address))) and "eighth bit"	USPAT	2003/07/01 12:46
11	0	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode") and "eighth bit"	USPAT	2003/07/01 12:47
12	1	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and ((source adj address) and (destination adj address))) and "eighth bit"	USPAT	2003/07/01 12:47
8	14	(((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode") and ((source adj address) and (destination adj address))	USPAT	2003/07/01 13:40

9	10	((((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode") and ((source adj address) and (destination adj address))) and (bit near address)	USPAT	2003/07/01 13:26
13	0	(((((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode") and ((source adj address) and (destination adj address))) and (bit near address)) and pixel	USPAT	2003/07/01 13:26
14	1	((((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "burst mode") and ((source adj address) and (destination adj address))) and pixel	USPAT	2003/07/01 13:26
15	1	345/530-574	USPAT	2003/07/01 13:53
16	0	((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and 345/570	USPAT	2003/07/01 13:54
17	33	345/570	USPAT	2003/07/01 13:54
18	4	345/570 and "burst mode"	USPAT	2003/07/01 13:57
19	108	345/568	USPAT	2003/07/01 13:56
20	3	345/568 and "burst mode"	USPAT	2003/07/01 14:44
21	0	((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "pixel interleaving"	USPAT	2003/07/01 14:44
22	0	345/568 and "pixel interleaving"	USPAT	2003/07/01 14:45
23	0	345/568 and "page interleaving"	USPAT	2003/07/01 14:45
24	0	((computer and memory) and (word and byte and bit) and (address same bit) and (MSB and LSB) and (source and destination) and @ad<20000727) and "page interleaving"	USPAT	2003/07/01 14:45
26	0	345/570 and "pixel interleaving"	USPAT	2003/07/01 14:45
25	1	345/570 and "page interleaving"	USPAT	2003/07/01 14:48
27	5523	365/230.01, 365/230.08, 365/230.09, 365/238.5, 365/239	USPAT	2003/07/01 14:49
28	139	345/570, 345/568	USPAT	2003/07/01 14:50
29	94	(345/570, 345/568) and pixel	USPAT	2003/07/01 14:50
30	7	((345/570, 345/568) and pixel) and ((source near destination) same address)	USPAT	2003/07/01 14:50